

9P2486

AMENDMENT TO INITIAL LETTER (Large Entity)		Docket No. 9295 (NCR.0020US)	
Applicant(s): WILLIAM F. WARD			
Serial No. 09/651,229	NOV 9 2002 Filing Date 08/25/2002	Examiner P.M. BATAILLE	Group Art Unit 2186

Invention: PROVIDING MULTIPLE MEMORY CONTROLLERS ON A MEMORY BUS

TO THE ASSISTANT COMMISSIONER FOR PATENTS: RECEIVED

Transmitted herewith is an amendment in the above-identified application.  
The fee has been calculated and is transmitted as shown below. NOV 21 2002  
Technology Center 2100

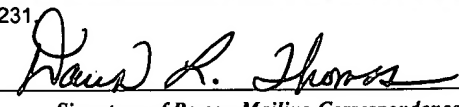
CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST # PREV. PAID FOR	NUMBER EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE	
TOTAL CLAIMS	30 -	23 =	7 x	\$18.00	\$126.00	
INDEP. CLAIMS	4 -	4 =	0 x	\$84.00	\$0.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					\$0.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$126.00	

- ☐ No additional fee is required for amendment.
- ☒ Please charge Deposit Account No. 50-1673(9295) in the amount of \$126.00  
A duplicate copy of this sheet is enclosed.
- ☐ A check in the amount of to cover the filing fee is enclosed.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1673(9295)  
A duplicate copy of this sheet is enclosed.
  - ☒ Any additional filing fees required under 37 C.F.R. 1.16.
  - ☒ Any patent application processing fees under 37 CFR 1.17.

  
Signature

Dated: Nov. 11, 2002

Dan C. Hu  
Reg. No. 40,025  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway  
Suite 100  
Houston, Texas 77024

I certify that this document and fee is being deposited on Nov 11, 2002 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231
 Signature of Person Mailing Correspondence
Dawn L. Thomas Typed or Printed Name of Person Mailing Correspondence

CC:



6/B  
5/B  
11-22-02

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: William P. Ward

Serial No.: 09/651,229

Filed: August 25, 2002

For: Providing Multiple Memory  
Controllers on a Memory Bus

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§

Group Art Unit: 2186

Examiner: P.M. Bataille

Atty. Dkt. No. 9295 (NCR.0020US)

Commissioner for Patents  
Washington, D.C. 20231

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REPLY TO OFFICE ACTION DATED AUGUST 12, 2002

Technology Center 2100

Dear Sir:

The Preliminary Amendment filed by the undersigned on December 1, 2000, provided an instruction to renumber original claim 27 to claim 33. That was incorrect. Original claim 27 should be renumbered to claim 23. Applicant notes that the Examiner has already properly indicated the correct numbering of this claim.

OBJECTIONS TO INFORMAL DRAWINGS

As suggested by Examiner, formal drawings will be submitted upon allowance.

IN THE CLAIMS:

Amend the following claims:

- 1 1. (Amended) A system comprising:
- 2 a memory bus; and
- 3 a plurality of memory controllers, each memory controller to generate
- 4 memory requests on the memory bus according to a predetermined priority scheme,
- 5 at least two of the plurality of memory controllers adapted to generate
- 6 concurrently pending memory requests on the memory bus.

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01 FC:1202 126.00 CH

Date of Deposit: November 11, 2002  
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington, DC 20231.  
Dawn L. Thomas  
Dawn L. Thomas